1.   (Currently Amended) A method comprising:

receiving an indication of a thermal event in a processor, the processor being part of a computer system;

in response to the indication of the thermal event, controlling a signal associated with a mechanical power switch of the computer system and, powering down the processor independently from the signal; and

subsequent to the powering down of the processor, powering down other components of the computer system in response to the signal.

2.   (Original) The method of claim 1, wherein said other components are located on a motherboard of the computer system.

3.   (Original) The method of claim 1, further comprising:

introducing a predetermined delay after the receiving before said powering down other components of the computer.

4.   (Cancelled)

5.   (Original) The method of claim 1, wherein said powering down the processor comprises:

cutting off a supply voltage to the processor.

6.   (Original) The method of claim 1, wherein said powering down other components comprises:

cutting off at least one supply voltage to said other components.

7.   (Currently Amended) A computer system comprising:

a processor capable of indicating a thermal event;

a mechanical switch, the switch being associated with a signal;

power consuming components;

a power supply subsystem to supply power to the processor and power consuming components; and

a circuit to:

in response to the indication of thermal event, control the signal and power down the processor independently from the signal~~receive an indication of a thermal event in the processor~~, and

power down other components of the computer system in response to the signal ~~indication, cause the power supply subsystem to power down the processor before powering down the power consuming components~~.

8.   (Original) The computer system of claim 7, wherein said power consuming components are located on a motherboard of the computer system.

9.   (Currently Amended) The computer system of claim 7, wherein the computer system introduces a delay in ~~power~~ powering down said power consuming components.

10.   (Cancelled)

11.   (Original) The computer system of claim 7, wherein the power supply subsystem powers down the processor by cutting off a supply voltage to the processor.

12.   (Currently Amended) The computer system of claim 7, wherein the power supply subsystem powers down the power consuming components by cutting off at least one supply voltage to said ~~other~~ power consuming components.

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13. (Currently Amended) A method comprising:

receiving an indication of a thermal event in a processor, the processor being part of a computer system;

in response to the indication, introducing a delay;

determining whether a mechanical power switch has been in an off position for the duration of the delay;

in response to the indication, powering down the processor before the expiration of the delay; and

~~powering down~~ powering down other components of the computer in response to the expiration of the delay.

14. (Original) The method of claim 13, wherein said other components are located on a motherboard of the computer system.

15. (Original) The method of claim 13, wherein said powering down other components comprises:

controlling a state of a signal indicative of a mechanical power switch of the computer system.

16. (Original) The method of claim 13, wherein said powering down the processor comprises:

cutting off a supply voltage to the processor.

17. (Original) The method of claim 13, wherein said powering down other components comprises:

cutting off at least one supply voltage to said other components.

18. (New) A method comprising:

receiving an indication of a thermal event in a processor, the processor being part of a computer system;

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in response to the indication of the thermal event, powering down the processor before powering down any other components of the computer system.

19. (New) The method of claim 18, wherein at least some of said any other components are located on a motherboard of the computer system.

20. (New) The method of claim 18, further comprising:

introducing a delay; and

determining whether a mechanical power switch has been in an off position for the duration of the delay.

21. (New) The method of claim 18, wherein said powering down the processor comprises:

removing a supply voltage to the processor.

22. (New) The method of claim 18, wherein said powering down any other components of the computer system comprises:

cutting off at least one supply voltage to said any other components of the computer system.

23. (New) A computer system comprising:

a processor capable of indicating a thermal event;

a power supply subsystem to supply power to the processor; and

a circuit to interact with the power supply subsystem to, in response to the processor indicating the thermal event, power down the processor before powering down any other components of the computer system.

24. (New) The computer system of claim 23, wherein the computer system introduces a delay in powering down said any other components of the computer system.

25. (New) The computer system of claim 23, wherein the power supply subsystem powers down the processor by removing a supply voltage to the processor.

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26.   (New) The computer system of claim 23, further comprising:

a mechanical switch associated with a signal to power on and off the computer system,

wherein the circuit controls the signal to power down said any other components of the computer system.